

190

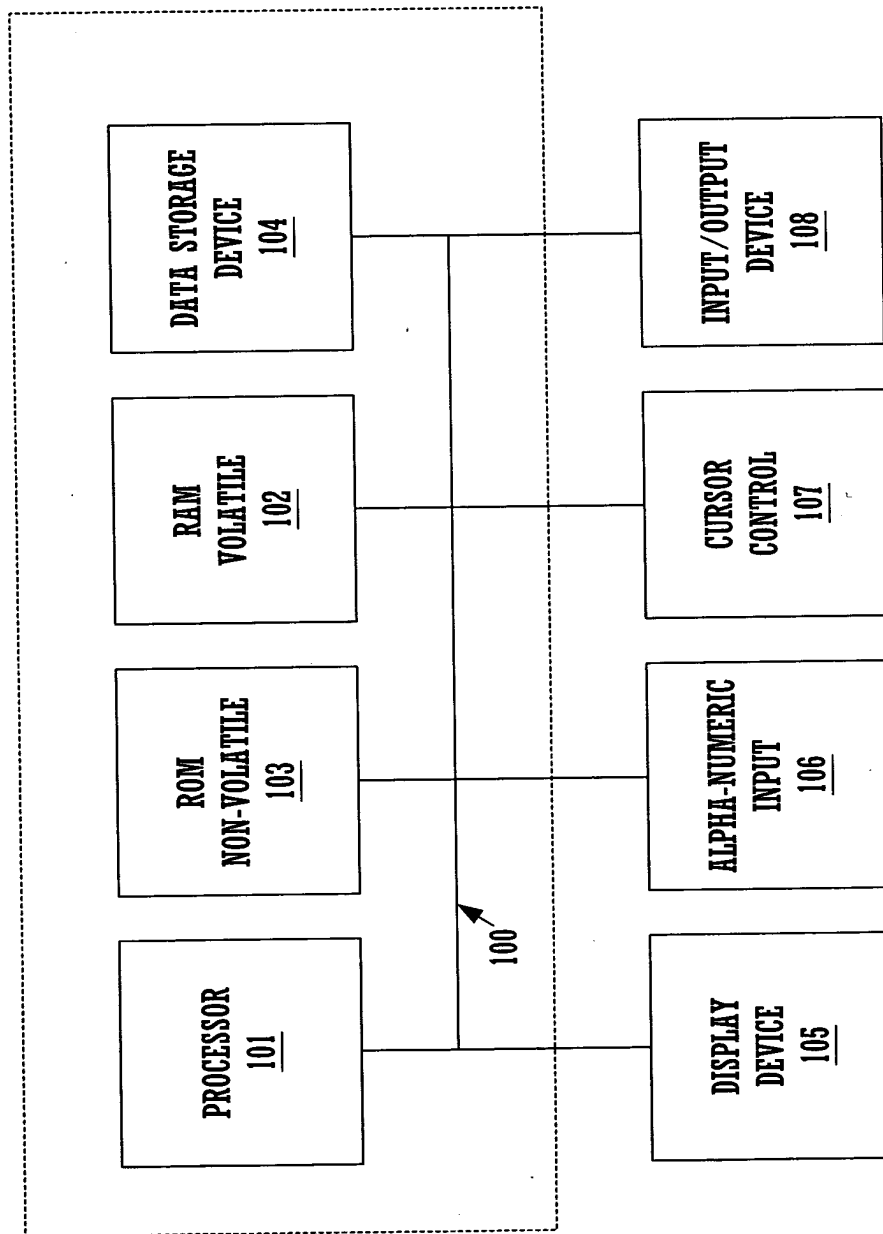


FIGURE 1

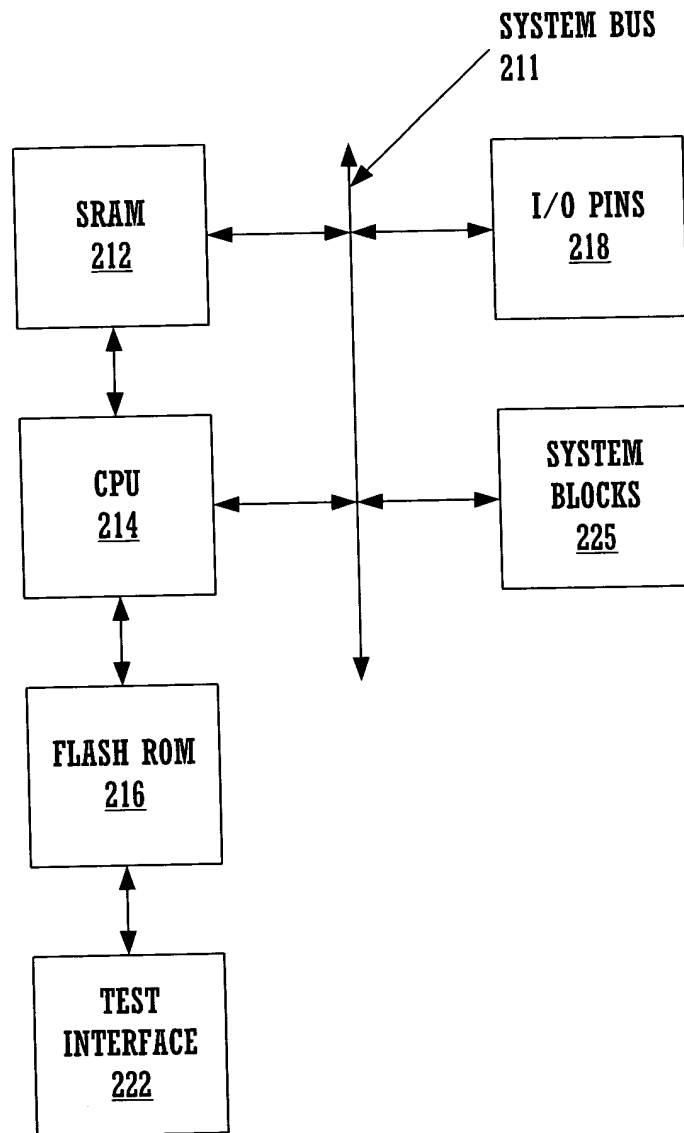
210

FIGURE 2A

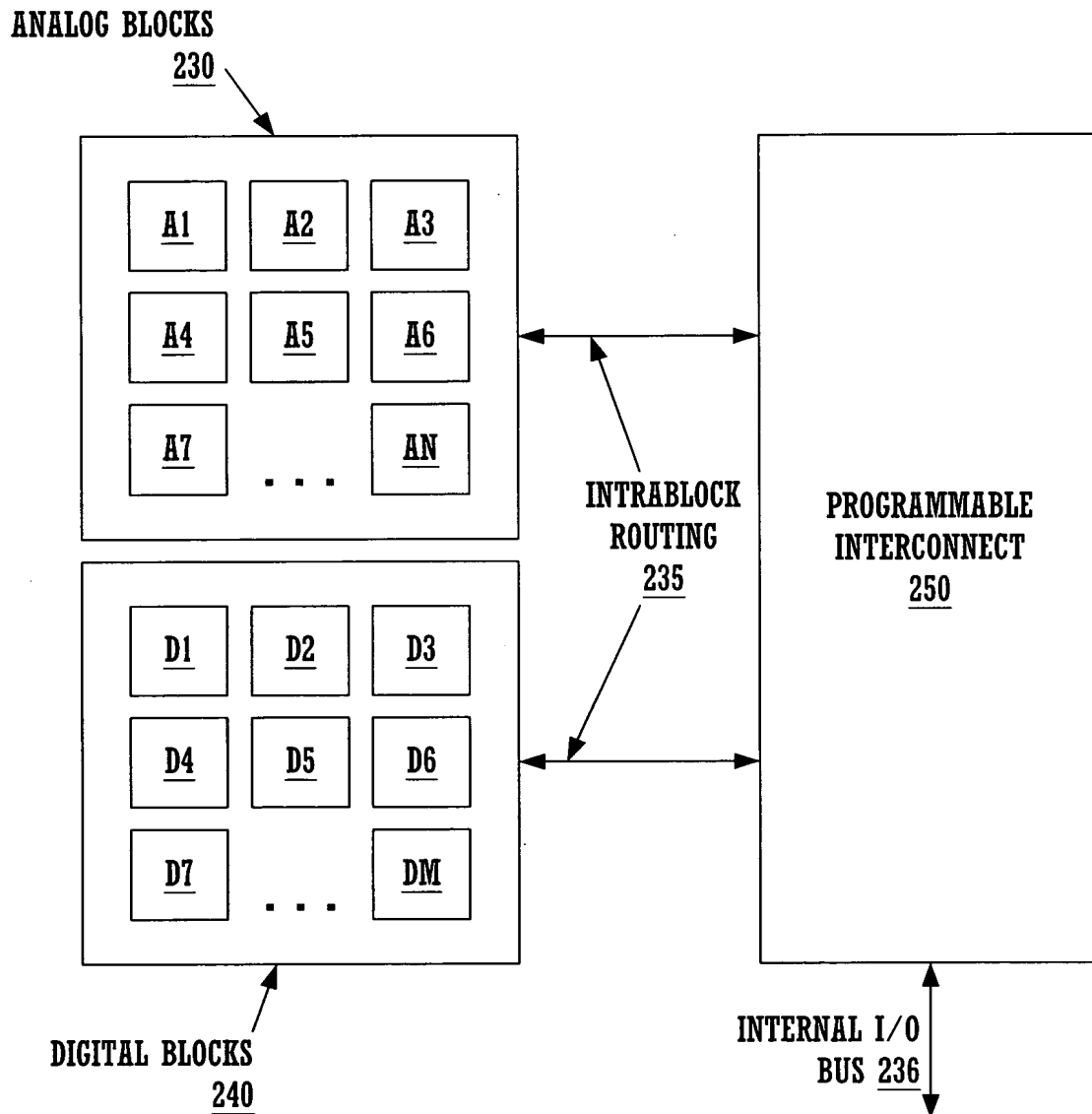
225

FIGURE 2B

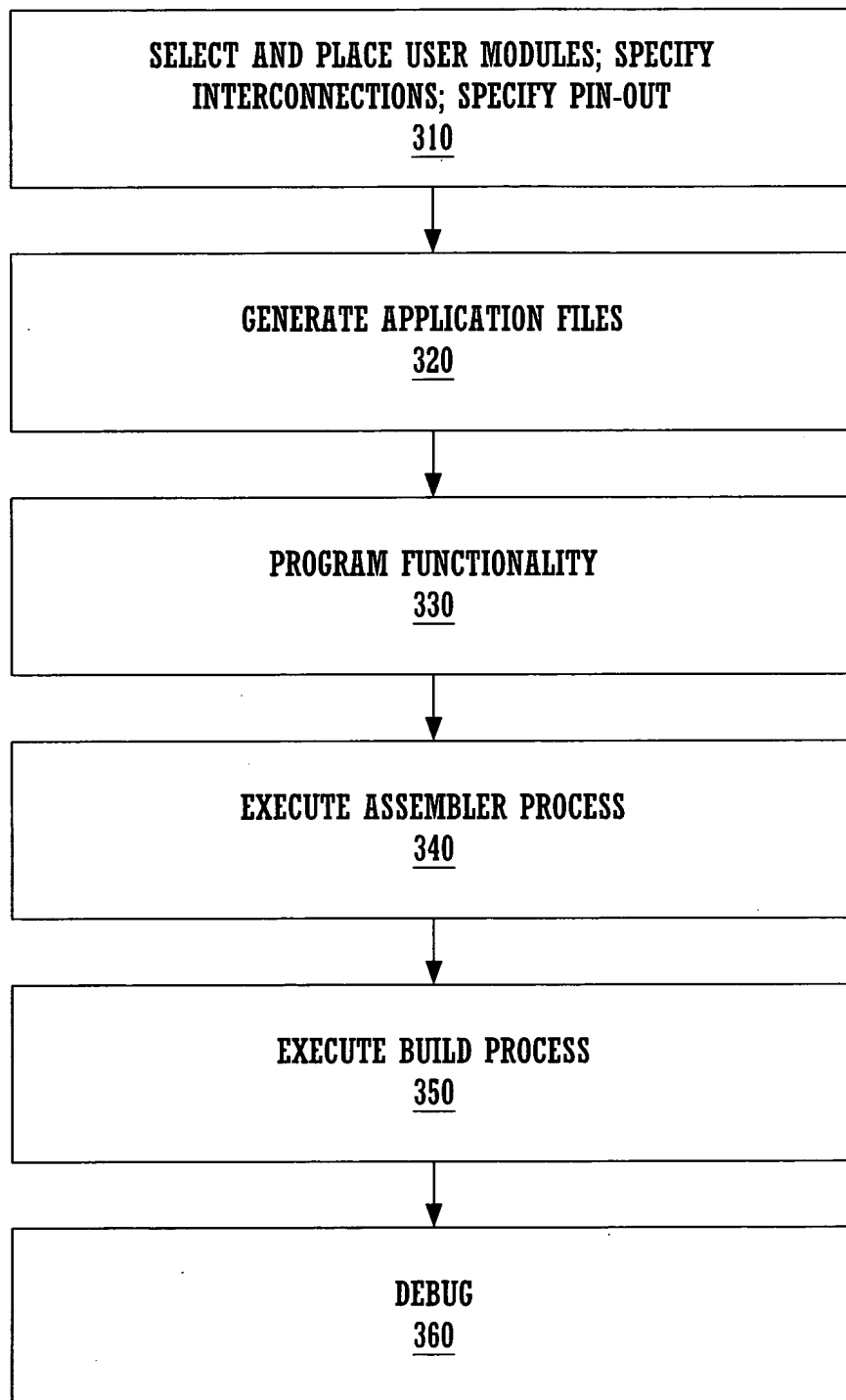
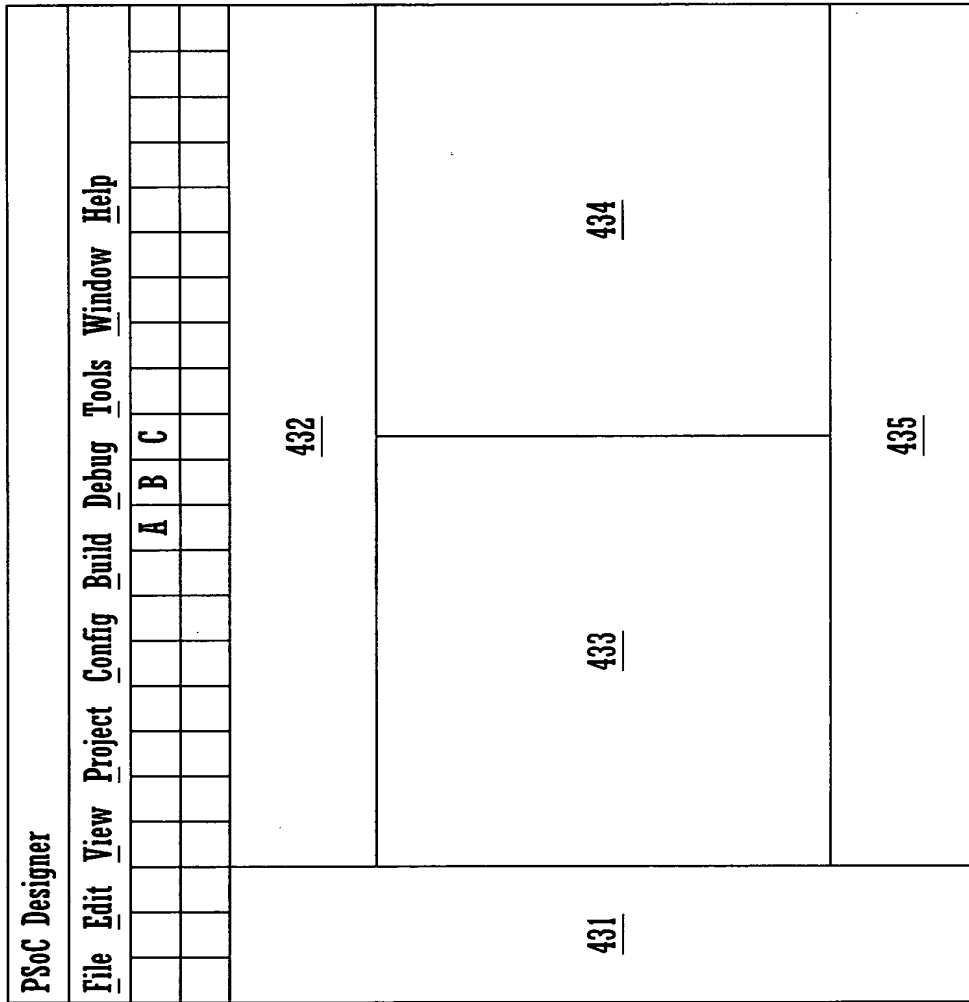
300

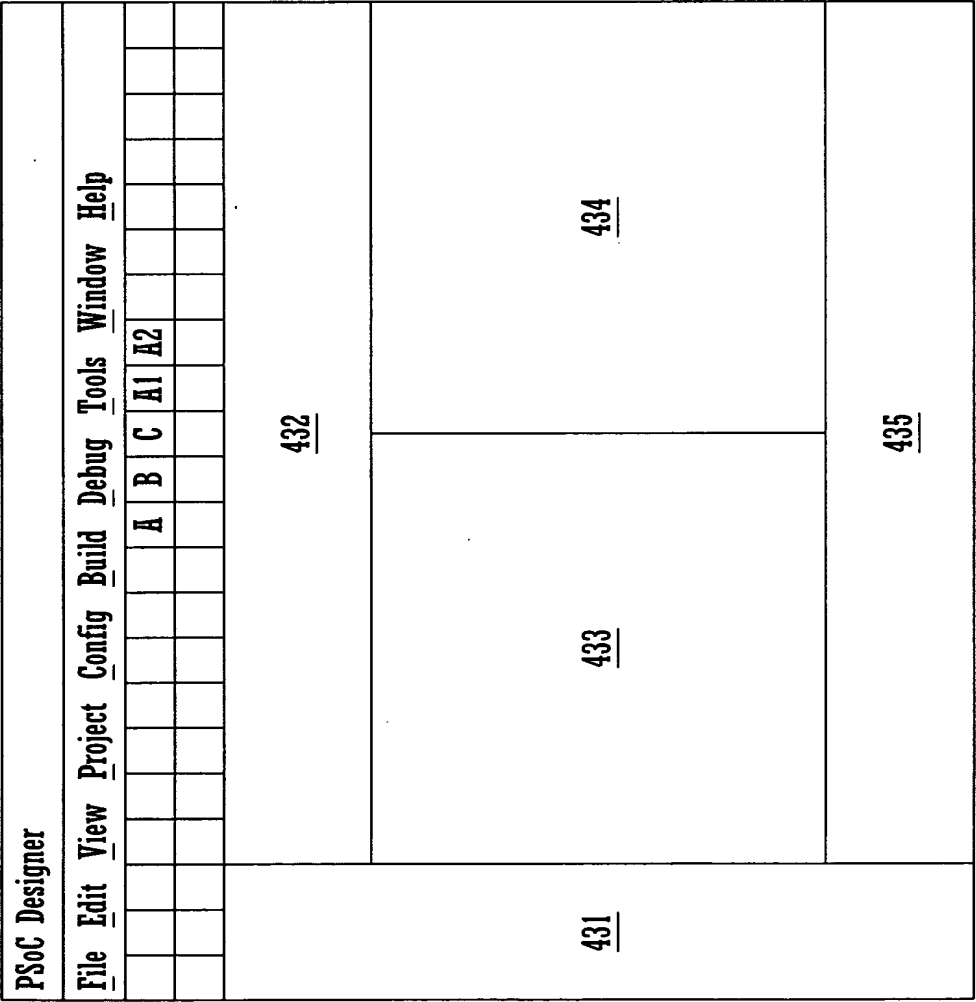
FIGURE 3



1st ELEMENTS 410

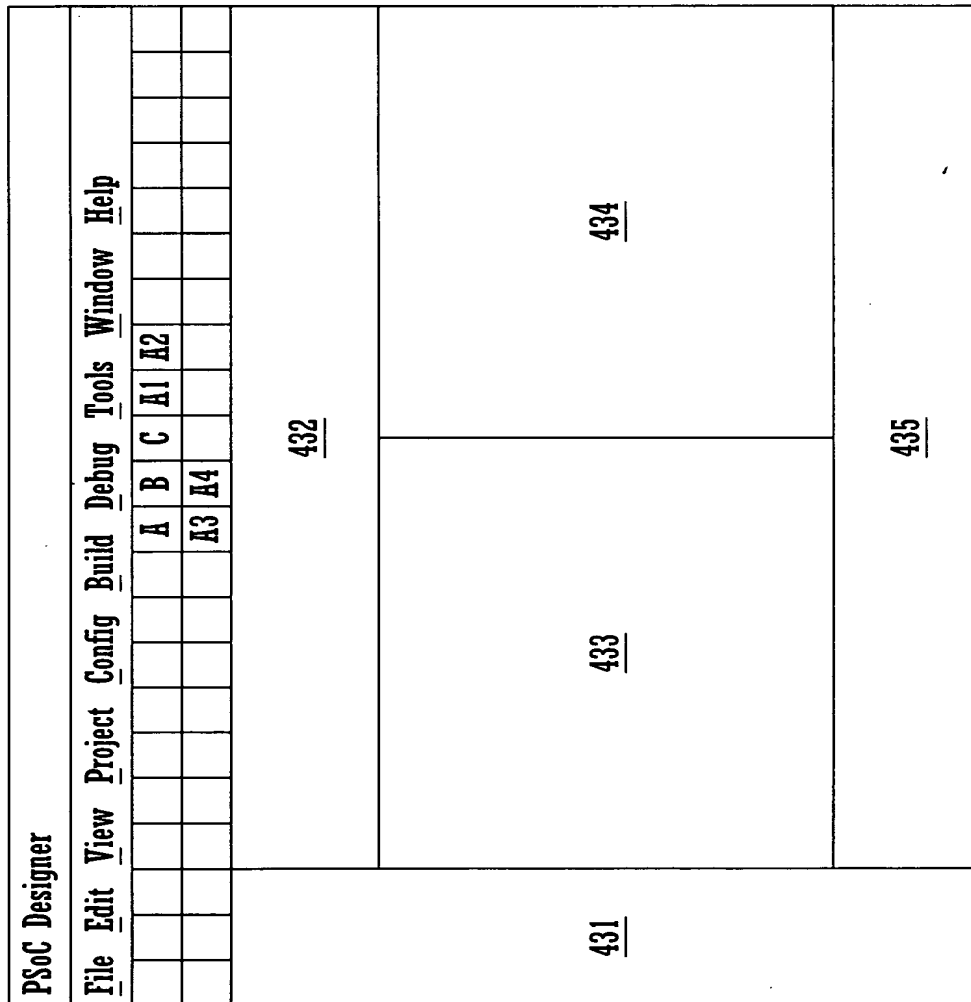
2nd ELEMENTS 420

FIGURE 4A



1st ELEMENTS 410  
2nd ELEMENTS 420

FIGURE 4B



1st ELEMENTS 410  
2nd ELEMENTS 420

FIGURE 4C

403

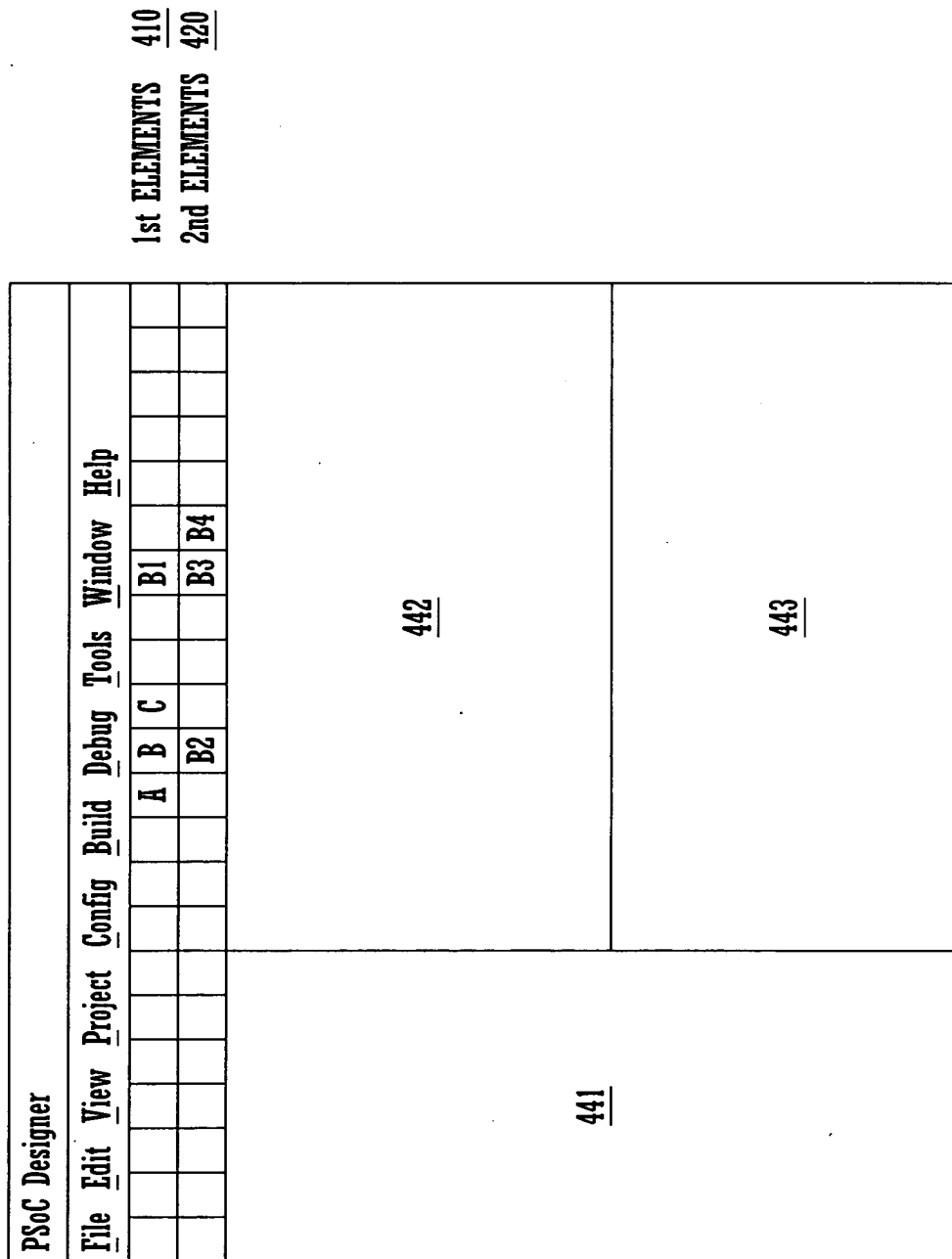


FIGURE 4D



500

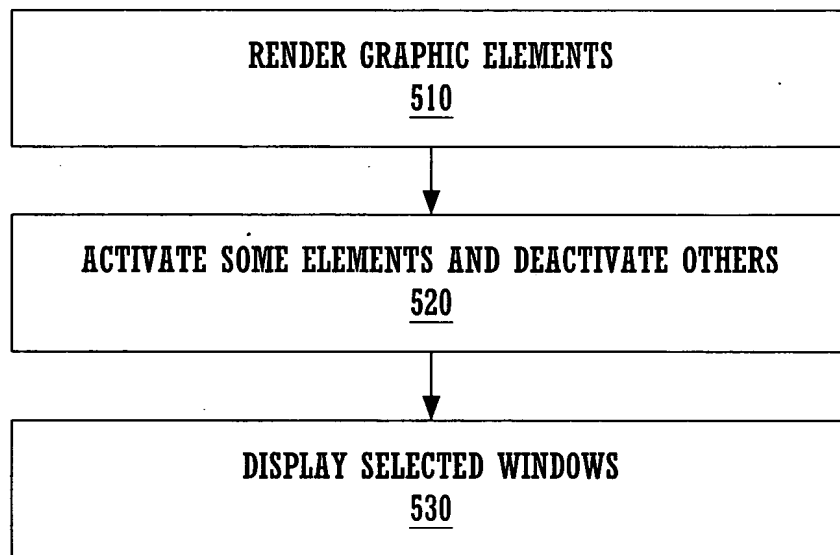


FIGURE 5

600

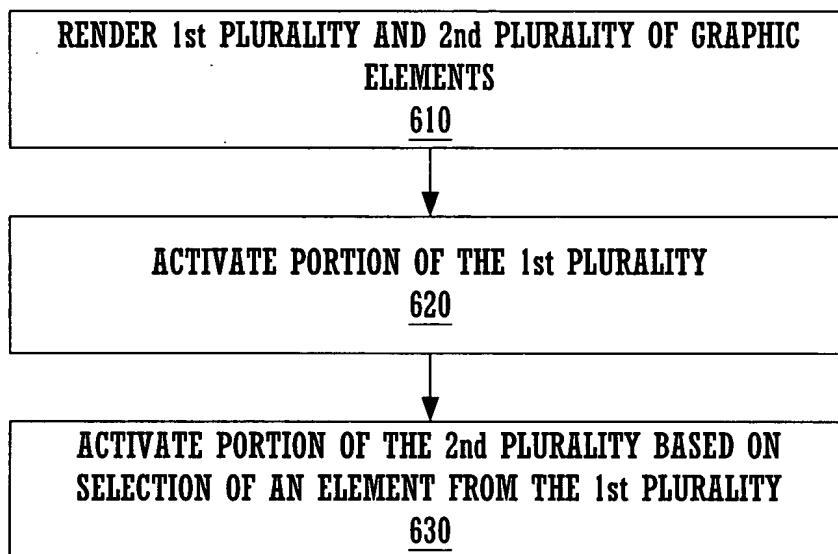


FIGURE 6

ADCs

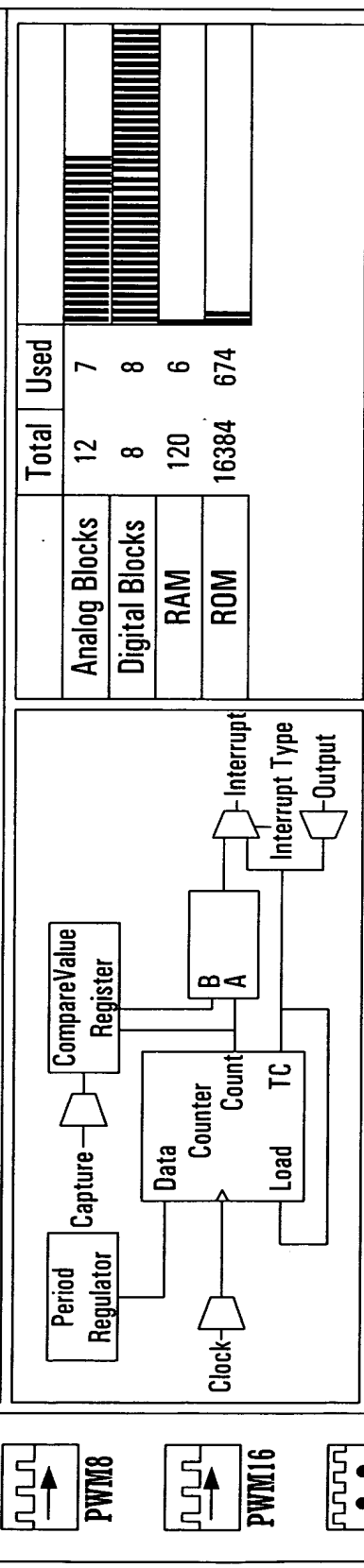
Counters

DACs

Amplifiers

PWMs

ADCINC12\_1 Counter16\_1 DAC8\_1 INSAMP\_1 INSAMP\_2 PWM16\_1 UART\_1



Cypress Microsystems

8-Bit Timer

Timer8 Revision A

Resources	Required	Optional
PSoC Blocks	1 Digital, 0 Analog	
Memory	48 bytes FLASH, 0 bytes SRAM	SRAM in Default Interrupt Routine
Pins		1 per External I/O

Resources

Overview

Diagram

Features

Description

Specs

Options

Restrictions

API

Sample Code

Examples

ReleaseNotes

FIGURE 7